

Electronic Patent Application Fee Transmittal

Application Number:	10707071			
Filing Date:	19-Nov-2003			
Title of Invention:	AUTOMATIC BIT FAIL MAPPING FOR EMBEDDED MEMORIES WITH CLOCK MULTIPLIERS			
First Named Inventor/Applicant Name:	Darren L. Anand			
Filer:	Steven Fischman/Julie Yanopulos			
Attorney Docket Number:	BUR920030168US1			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Claims in excess of 20	1202	3	50	150
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				150